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Alexandria, VA 22313-1450

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of: Arash Hassibi, et al.) Examiner: Quang D. Vu
Application No: 09/843,486) Art Unit: 2811
Filed: April 25, 2001)
For: Optimal Simultaneous Design and Floorplanning Of Integrated Circuit) } }
Commissioner For Patents P.O. Box 1450	

SUPPLEMENTAL AMENDMENT TO REQUEST FOR CONTINUED EXAMINATION FILED MAY 27, 2003

Sir:

Please enter the following amendments and consider the following remarks.

CERTIFICATE OF TRANSMISSION

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